

OEM 8x32 v.2



product line

This multiplexed architecture 8x32 (v2) provides an efficient and cost-effective solution to Original Equipment Manufacturers for integration into third-party systems. The board and dynamic-link libraries (dlls) are available.



Option 1: board without probe



Option 2: board with I-PEX connector



Option 3: board with Hypertronix connector

CONFIGURATIONS : 8x32

pulsers	Adjustable voltage: +/-10 to +/-50V with 1V step Bipolar rectangular pulse, adjustable width: 20 ns to 1280 ns, step of 2.5 ns Rise time < 10 ns (80V, 50 Ω) Max. PRF: 30 KHz
receivers	Bandwidth: 0,5Mhz – 20Mhz Adjustable gain from 0 to 90 dB Adjustable analog DAC on 36 dB (max. 40 dB/μs) synchronized on events Cross-talk between two channels > 45 dB, max. input signal amplitude: 1 Vpp
digitizer	Max. sampling frequency: 100 MHz - Adjustable from 100 MHz to 6.6 MHz Summed data resolution: 16 bits Input impedance: 50 Ω Global delay: 0 up to 1.6 ms, step of 10 ns Delay-laws at transmission/reception: 0 to 20 μs, step of 2.5 ns Digitizing depth: up to 50,000 samples (8,000 samples max. per elementary channel) FIR filters
embedded processors	Programable FPGA
hardware configuration	Multiplexed architecture: 8x32
libraries	dll provided
dimensions	L x W x H: 92mm x 65mm x 13mm Weight: 51g
I-O	Option 1: board without probe connector Option 2: board with I-PEX connector Option 3: board with Hypertronix connector 3 encoders input, 1 external trigger input, 1 general purpose I-O 8 global TTL input, 8 global TTL output Powered by USB2